FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 174/205	APPLICATION NO. 09/826,527
	INFORMATION DISCLOSURE STATEMENT BY APPLICANT	APPLICANT Martin Langhammer	
		FILING DATE April 4, 2001	GROUP 2121

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U.S. PATENT DOCUMENTS

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EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	TRANSLATION NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER INITIAL	
MM	A. Chhabra and R. Iyer, "A Block Floating Point Implementation on the TMS320C54x DSP", Application Report SPRA610, Texas Instruments, December 1999, pp. 1-10.
MM	The Applications Engineering Staff of Analog Devices, DSP Division, Digital Signal Processing Applications Using the ADSP-2100 Family (edited by Amy Mar), Prentice-Hall, Inc., Englewood Cliffs, NJ, Copyright 1990 by Analog Devices, Inc., Norwood, MA, pp. 141-192.
MM	"TMS320C54x DSP Reference Set, Volume 1: CPU and Peripherals", Literature Number: SPRU131F, Texas Instruments, April 1999, pp. 2-1 through 2-16 and 4-1 through 4-29.

EXAMINER MM DATE CONSIDERED 5/2004

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	4,871,930	10/03/89	Wong et al.	307	465	
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MLa	0 461 798 A2	12/18/91	EPO	H03K	19/177		
MLa	2 283 602 A	05/10/95	GB	H03K	19/177		

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